



Alaa Abdullah, RLT Assistant Professor

Department of Electrical and Computer Engineering

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▼ Education

- Ph.D., Ryerson University, Toronto, Canada, June 2014.
- M.A.Sc., Ryerson University, Toronto, Canada, June 2010.
- B.A.Sc., University of Technology, Baghdad, Iraq, August 1989.

Teaching Expertise/Courses

Dr. Abdullah's teaching expertise includes Digital logic Design, Electric Circuit Analysis, Communication Systems, Advanced Analog/Digital Circuit Design, Very Large Scale Integrated (VLSI) Circuit Design, Microelectronics Circuit Design., Communication Systems, Introduction to Signal Processing.

Research Expertise

Dr. Abdullah's research interests include CMOS Analog and Mixed-Signal Circuits Design, Design Methodologies for Gb/s Wire-Line Transceivers of Data Links, On-Chip interconnect Design for High-Speed Serial Links, 2PAM-4PAM Near and Far Ends Channel Equalization, VLSI Circuits Design.

▼ Sample Publications

- Alaa Al-Tae and F. Yuan, , "An Edge-Based Dual Adaptive Decision Feedback Equalizer for Gbps Serial Links," Analog integrated Circuits and Signal Processing (Springer), Vol.90, No.2, pp.399-409, Feb. 2017.
- Alaa R. Al-Tae and F. Yuan, "Adaptive Decision Feedback Equalizer with Hexagon EOM and Jitter Detection", Circuits, Systems, and Signal processing, Vol. 35, No.7, PP. 2487-2501, Jul. 2016.
- Alaa R. Al-Tae, F. Yuan and A. Ye, "Minimum jitter adaptive decision feedback equalizer for Gbps serial links," IET Journal of Engineering, Vol.2, No.1, pp. 1-7, Jan. 2015.
- Alaa R. AL-Tae, F. Yuan, and A. Ye, "An improved RC model for VLSI interconnects with its applications to buffer insertion," Analog Integrated Circuits and Signal Processing (Springer), Vol.79, No.1, pp. 105-113, April 2014.
- F. Yuan, A. Al-Tae, A. Ye, and S. Sadr, "Design techniques for decision feedback equalization of multi-Gbps serial data links: a state-of-the-art review," IET Circuits, Devices, & Systems. Vol.8, No.2, pp.118-130, 2014.
- A. Al-Tae, F. Yuan, A. Ye, and S. Sadr, "New 2D eye-opening monitor for Gbps serial links," IEEE Transactions on Very Large Scale Integration Systems, Vol.22, No.6, pp. 1209-1218, June 2014.
- Alaa R. AL-Tae, F. Yuan, and A. Ye, "A Power-Efficient 2-Dimensional On-Chip Eye-Opening Monitor for Gbps Serial Links," Analog Integrated Circuits and Signal Processing (Springer), Vol. 76, No. 1, pp. 117-128, July 2013.
- Alaa R. AL-Tae, F. Yuan, and A. Ye, "A new power-efficient CDMA-based transmitter for high-speed serial links," Analog Integrated Circuits and Signal Processing, Vol.71, No.2, pp.1-7, Feb. 2012. 1.